

A RELATIVE COMPARISON AMONG RECENT TOPOLOGIES OF SINGLE-PHASE NON-ISOLATED AC-DC BUCK-BOOST CONVERTER BASED ON OPEN-LOOP PERFORMANCE ANALYSIS

Istiak Ahmed and Muhibul Haque Bhuyan

Abstract-In the present article, a family of power electronic single-phase, non-isolated ac-dc buck-boost converters based upon the open-loop performance analysis have been reviewed. In the literature, abundant kinds of ac-dc buck-boost converter topologies have been searched. Among them, a few important topologies are conversed here and matched with several parameters like voltage gain, efficiency, input power factor, total harmonic distortion, and the number of component counts with the variation of load resistance and duty cycles. The assessment indicates that the input switched and switched capacitor buck-boost converters maintain an impressive input power factor of 0.99 at some specific load or duty cycles. In terms of efficiency, the high-efficiency buckboost converter can provide over 99% efficiency. The switched capacitor buck-boost converter can provide the Total Harmonic Distortion (THD) of input current less than unity.

Keywords—Buck-boost converter topology, total harmonic distortion, switching frequency, voltage gain.

I. INTRODUCTION

WITH the progress in the new technical and industrial epoch, reliability, efficiency, size, and quality of voltage translation from AC-AC, DC-DC, AC-DC, or DC-AC has become important factors. With the appropriate designs, these translators have many significant aspects in the fields of power electronics [1].

Whereas a buck converter can assist in step-down the DC signal to another lower level of DC signal [2], a buck-boost converter is a type of DC-DC converter that can do both that can help to step-up or step-down the voltage level by stepping down or up the current level respectively from its input voltage to its output voltage level at the load side. Such types of circuits contain at

least two semiconductor devices, such as a diode and a transistor. However, the modern buck-boost converter circuits use another transistor instead of the diode which is used to obtain synchronous rectification. The buck-boost converter circuits also contain at least one energy-storing component like either a capacitor or an inductor, or in some cases the amalgamation of both. To diminish the ripples in the supply voltage, filter circuits are normally used both at the supply and load sides of the buck-boost converter circuits. Filter circuits are made of using either a capacitor or an inductor or both. When the filter circuit is used at the input and output terminals then they are called the supply-side and load-side filters respectively [3].

There are two distinct types of buck-boost converter circuit's modes, such as 1) continuous conduction mode in which the inductor current certainly doesn't become zero at its end and thus the inductor discharges partially before the end of the switching cycles, and 2) discontinuous conduction mode in which the inductor current become zero at its end and thus the inductor will release its energy completely when the switching cycles terminate [4-6].

There are several advantages of using the Buck-Boost converter circuit, such as it has a simple structure, it is easy to implement, it can provide greater output voltage, it can operate on short duty cycle, it can operate on small voltage with MOSFETs, etc. [4].

The single-phase AC-DC buck-boost converter circuit has many applications in the fields of power electronics. It can be used in applications, such as electronic devices used in cars, self-regulating power supplies, consumer electronics, battery-powered systems, adaptive control systems, power amplifier circuits, portable devices, etc., where the range of the battery's output voltage is significantly huge, buckboost converters are essential [4]. It can also be used for wind energy control [7], power factor correction [8],

I. Ahmed is with the Department of Electrical and Electronic Engineering, Southeast University, Dhaka, Bangladesh (e-mail: *iahmed@seu.edu.bd*).

M. H. Bhuyan is with the Department of Electrical and Electronic Engineering, Southeast University, Dhaka, Bangladesh (e-mail: *muhibulhb@seu.edu.bd*).



residential solar PV [9], and electric vehicle battery life extension circuits [10]. It is exclusively appropriate as a front-end power supply source in variable-speed drive schemes to translate the service source potential into an adjustable dc-link voltage supply [11].

II. TOPOLOGIES AND OPERATIONS OF THE AC-DC BUCK-BOOST CONVERTER

A converter is the basic element of a power electronics system. It employs power semiconductor devices and possible energy storage elements like inductors and capacitors. The task of the AC-DC converter is to translate the input AC to output DC. There has been a deep study in switched-mode power converters. They are classified as single inductor converters like Buck, Boost, and Buck-Boost converters as well as double inductor converters like Cu k [12-13], SEPIC [14], Sheppard-Taylor [15], and Zeta [16] converters, etc. The function of AC-DC Buck-Boost converters is to both step up and step down the input signal. Throughout the decades, there have been modifications to improve different aspects of the traditional AC-DC buck-boost converter. Specific converter topologies have been developed to meet desired load characteristics. In this paper, an analysis with a comparative review of three different recently developed topologies of the single-phase non-isolated AC-DC buck-boost converters is presented. In the following three subsections, various topologies and operations of different types of AC-DC buck-boost converters are discussed.

A. High-Efficiency AC-DC Buck-Boost Converter

A schematic circuit diagram of the high-efficiency AC-DC buck-boost converter is presented in Fig. 1. It produces an output voltage with the same polarity as the input signal. No separating device in between the source and the load sides has been employed here. The circuit comprises three inductors (L_1 to L_3), three capacitors (C_1 to C_3), six diodes (D_1 to D_6), and a switch (M_1). The inductor L_1 and the capacitor C_1 constitute the passive type input filter. The inductors L_2 and L_3 in the middle are used here as the buck-boost inductors. Capacitors C_2 and C_3 at the output terminal are called the load capacitor, and the resistor R acts as a load [17].



Fig. 1. High-efficiency AC-DC Buck-Boost Converter [17]

The open-loop analysis of the circuit is done using PSIM 9.1.1. MOSFET has been used as the switching device. Input voltage (V_{in}) peak is selected 300 V. The switching frequency is fixed at 10 kHz. The values of L_1 , L_2 , and L_3 are chosen as 5 mH, 1 mH, and 1 mH. The values of C_1 , C_2 , and C_3 are chosen as 1 μ F, 110 μ F, and 110 μ F. The load resistor *R* is 100 Ω [17].

B. Input Switched AC-DC Buck-Boost Converter

The schematic circuit diagram of the input switched AC-DC buck-boost converter is presented in Fig. 2. There are three inductors and two capacitors in the circuit. As the input filter inductor, L_3 and capacitor, C_2 is used. The other two inductors operate as buck-boost inductors. The circuit has ten diodes. Capacitor, C_1 is the output capacitor and resistor R_1 is used as load [18].



Fig. 2. Input Switched AC-DC Buck-Boost Converter [18]

The open-loop analysis of the circuit is done using PSIM 9.1.1. An IGBT is used as the switching device. Input voltage (V_{in}) peak is selected 300 V. The switching frequency is fixed at 10 kHz. The values of L_1 , L_2 , and L_3 are chosen as 5 mH. The values of C_1 and C_2 are chosen as 2.5 µF and 5 µF. The load resistor R_1 is 100 Ω [18].

C. Switched Capacitor AC-DC Buck-Boost Converter

The schematic circuit diagram of the switchedcapacitor AC-DC buck-boost converter is depicted in Fig. 3. The topology of the buck-boost circuit has two stages. In the first stage, a full-wave bridge rectifier circuit is inserted using p-n junction diodes. In the second stage, a switched-capacitor DC-DC topology is used to be operated at a high frequency. The circuit has two inductors among which inductor L_1 is used as the Buck-Boost inductor. There are four capacitors in the circuit. Inductor L_{in} along with capacitor C_{in} constitutes the input filter. Capacitor C_1 is used as the output capacitor and resistor R_1 is used as the load. The circuit has a switch and a total of eight diodes [19].





Fig. 3. Switched Capacitor AC-DC Buck-Boost Converter [19]

The open-loop analysis of the circuit is done using PSIM 9.1.1. MOSFET is used as the switching device. Input voltage (V_{in}) peak is selected 300 V. Switching frequency is set at 10 kHz. The values of L_{in} and L_1 are chosen as 40 mH and 400 μ H. The values of C_{in} , C_1 , C_2 , and C_3 are chosen as 50 μ F, 50 μ F, 1 μ F, and 1 μ F respectively. The load resistor R_1 is 100 Ω [19].

III. RESULTS AND DISCUSSIONS ON THE PERFORMANCE COMPARISON OF VARIOUS TOPOLOGIES

Based on the open-loop analysis of different topologies of the single-phase AC-DC buck-boost converter (BBC) circuits, a performance comparison is done. Table 1 provides a general comparison of three different topologies, such as high-efficiency BBC, input switched BBC and switched capacitor BBC, used in this study. For each of these topologies, the number of components, viz. switch, diode, inductor, and capacitor used in the single-phase AC-DC buck-boost converter circuits is mentioned.

TABLE I GENERAL COMPARISON ON DIFFERENT TOPOLOGIES OF AC-DC BUCK-BOOST CONVERTER (BBC)

			. ,	
Topology	Figure #	Ref. #	Switch/ Capacitor Count	Diode/ Inductor Count
High-efficiency BBC	1	17	1/3	6/3
Input Switched BBC	2	18	1/2	10/2
Switched Capacitor BBC	3	19	1/4	8/2

A. Performance comparison based on duty cycle variation

The open-loop performance comparison on different topologies of AC-DC Buck-Boost converter is done based on duty cycle variation. The overall efficiency, input power factor, total harmonic distortion (THD) of the input current, and the voltage gain are analyzed with the variation of the duty cycle. A load of all the converters is chosen as 100 Ω and the switching frequency of M_1 is fixed at 10 kHz. The data is provided in Table 2. For a better understanding of the comparison, a column chart and line chart are provided.

TABLE II PERFORMANCE COMPARISON ON DIFFERENT TOPOLOGIES OF SINGLE-PHASE NON-ISOLATED AC-DC BUCK-BOOST CONVERTER BASED ON DUTY CYCLE VARIATION

Duty	Tomalare	Efficiency	Power	THD	Voltage	
Cycle	1 opology	(%)	Factor	(%)	Gain	
	High-efficiency BBC	97.587	0.712	40.295	0.238	
0.1	Input Switched BBC	74	0.94	3	0.64	
	Switched Capacitor BBC	80	0.12	23.3	0.42	
	High-efficiency BBC	99.061	0.767	57.147	0.548	
0.2	Input Switched BBC	81.28	0.97	2.7	0.73	
	Switched Capacitor BBC	90.88	0.39	0.8	0.84	
	High-efficiency BBC	99.288	0.748	61.188	0.854	
0.3	Input Switched BBC	87.34	0.97	3.15	0.79	
	Switched Capacitor BBC	93.46	0.73	0.25	1.25	
	High-efficiency BBC	99.276	0.812	50.792	1.35	
0.4	Input Switched BBC	92.54	0.97	3.28	0.84	
	Switched Capacitor BBC	94.72	0.94	0.20	1.62	
	High-efficiency BBC	99.158	0.901	37.724	2.08	
0.5	Input Switched BBC	97.16	0.98	2.7	0.89	
	Switched Capacitor BBC	95.25	0.99	0.14	1.90	
	High-efficiency BBC	99.045	0.967	25.646	3.009	
0.6	Input Switched BBC	98.62	0.99	2	1.33	
	Switched Capacitor BBC	95.49	0.95	0.27	2.08	
	High-efficiency BBC	98.719	0.969	15.753	4.147	
0.7	Input Switched BBC	98.35	0.99	1.7	2.06	
	Switched Capacitor BBC	95.99	0.78	4.5	2.20	
	High-efficiency BBC	98.025	0.823	7.332	5.322	
0.8	Input Switched BBC	96.98	0.93	1	3.28	
	Switched Capacitor BBC	95.95	0.40	3.18	1.69	
	High-efficiency BBC	95.292	0.342	1.535	4.02	
0.9	Input Switched BBC	86.40	0.5	0.4	3.51	
0.9	Switched Capacitor BBC	93.06	0.10	24.2	0.77	



Fig. 4. Graphical comparison of efficiency between different topologies of single-phase non-isolated AC-DC buck-boost converter under duty cycle variation



Fig. 5. Graphical comparison of power factor between different topologies of single-phase non-isolated AC-DC buck-boost converter under duty cycle variation





Fig. 6. Graphical comparison of THD between different topologies of single-phase non-isolated AC-DC buck-boost converter under duty cycle variation

B. Performance comparison based on load variation

The open-loop performance comparison on different topologies of AC-DC buck-boost converter is done based on load variation. The overall efficiency, input power factor, total harmonic distortion (THD) of the input current, and the voltage gain are analyzed with the variation of load. The switching frequency is fixed at 10 kHz and the duty cycle is chosen as 0.6. The data is provided in Table 3. For a better understanding of the comparison, a column chart and line chart are provided.

TABLE III PERFORMANCE COMPARISON ON DIFFERENT TOPOLOGIES OF SINGLE-PHASE NON-ISOLATED AC-DC BUCK-BOOST CONVERTER BASED ON LOAD VARIATION

Load (Ω)	Topology	Efficiency (%)	Power Factor	THD (%)	Voltage Gain
	High-efficiency BBC	99.21	0.97	23.78	2.10
50	Input Switched BBC	98.86	0.99	1.75	1.30
	Switched Capacitor BBC	98.45	0.91	5.22	1.59
	High-efficiency BBC	99.045	0.967	25.646	3.009
100	Input Switched BBC	98.62	0.99	2	1.33
	Switched Capacitor BBC	95.49	0.95	0.27	2.08
	High-efficiency BBC	99.05	0.95	30.04	3.57
150	Input Switched BBC	98.44	0.98	4.30	1.34
	Switched Capacitor BBC	98.88	0.95	0.15	2.59
	High-efficiency BBC	98.93	0.94	35.74	4.01
200	Input Switched BBC	97.82	0.97	6.66	1.35
	Switched Capacitor BBC	98.65	0.95	0.15	3.00
	High-efficiency BBC	98.65	0.92	40.53	4.31
250	Input Switched BC	97.78	0.96	9.2	1.37
	Switched Capacitor BBC	98.40	0.95	0.15	3.36
	High-efficiency BBC	98.54	0.92	41.43	4.41
300	Input Switched BBC	97.70	0.95	11.1	1.41
	Switched Capacitor BBC	98.16	0.95	0.16	3.68
	High-efficiency BBC	98.46	0.92	40.50	4.45
350	Input Switched BBC	97.76	0.96	11.1	1.50
	Switched Capacitor BBC	97.90	0.95	0.15	3.97
	High-efficiency BBC	98.36	0.92	39.06	4.48
400	Input Switched BBC	97.72	0.97	9.52	1.59
	Switched Capacitor BBC	97.67	0.95	0.15	4.24
	High-efficiency BBC	98.45	0.92	37.17	4.51
450	Input Switched BBC	97.67	0.97	6.81	1.68

Load (Ω)	oad A) Topology		Effic (%	iency ⁄6)	Power Factor	THD (%)		Voltage Gain			
	Switched Capacitor BBC High-efficiency BBC Input Switched BBC				97	.43	0.95	0.16		4.49	
					98.15 97.65		0.93	34.93	; ·	4.53 1.77	
500							0.97	4.78			
	Switched Capacitor BBC			97	.17	0.95	0.15		4.73		
100 99 98 97 95 95 95 94 93		High-effi	ciency BBC		t Switched	BBC	Switched C	apacitor B	3C		
	50	100	150	200	250 Load	300 d(Ω)	350	400	450	500	

Fig. 7. Graphical comparison of Efficiency between different topologies of single-phase non-isolated AC-DC buck-boost converter under load variation



Fig. 8. Graphical comparison of Power Factor between different topologies of single-phase non-isolated AC-DC buck-boost converter under load variation



Fig. 9. Graphical comparison of THD between different topologies of single-phase non-isolated AC-DC buck-boost converter under load variation

IV. CONCLUSION

This research article represents a comprehensive open-loop analysis of three different topologies of single-phase non-isolated AC-DC buck-boost converter (BBC). Performances are compared by varying the duty cycle from 10% to 100% and load resistance from 50 Ω



to 500 Ω for the different topologies. From this comparative analysis, it is realized that the highefficiency BBC demonstrates better efficiency of over 99% than that of the other BBC upon the duty cycle or load resistance variation. The input switched and switched capacitor BBCs provide a better power factor than that of the high-efficiency BBC upon the duty cycle or load resistance changes. When the THD of the input current is considered, it has been observed that the switched capacitor BBC can provide smaller values of THD than that of the other two BBCs upon duty cycle or load variation.

REFERENCES

- [1] I. Ahmed and M. H. Bhuyan, "A Critical Review on Open Loop Analysis of Single-Phase Non-Isolated AC-DC Buck Converters," Southeast University Journal of Electrical and Electronic Engineering (SEUJEEE), ISSN: p-2710-2149, e-2710-2130, vol. 1, issue 1, January 2021, pp. 7-12.
- [2] K. Jayaswal, D. K. Palwalia, G. Jain, P. Kumar, "Design-Oriented Analysis of Non-isolated DC-DC Buck Converter," *Ciência e Técnica Vitivinícola*, Portugal, ISSN: 0254-0223, vol. 30, no. n. 2, January 2015, pp. 177-213.
- [3] V. Chellappa, J. Gnanavadivel, and N. S. Kumar, "Power Quality Improvement Techniques in AC-DC Cuk Converter," 2011 International Conference on Emerging Trends in Electrical and Computer Technology (ICETECT), 2011, pp. 430-435.
- [4] X. Zhou and Q. He, "Modeling and Simulation of Buck-Boost Converter with Voltage Feedback Control," MATEC Web of Conferences 2015, EDP Sciences, doi: 10.1051/matecconf/ 20153110006.
- [5] S. Siouane, S. Jovanovi'c and P. Poure, "Service Continuity of PV Synchronous Buck/Buck-Boost Converter with Energy Storage," Energies, 2018, vol. 11, p. 1369; doi:10.3390/ en11061369.
- [6] R. W. Erickson and D. Maksimović, "The Discontinuous Conduction Mode. In: Fundamentals of Power Electronics," July 2020, Springer, Cham, https://doi.org/10.1007/978-3-030-43881-4_5.
- [7] A. Mittal and K. Arora, "Control of Wind Energy by Using Buck-Boost Converter," International Journal of Emerging Technology and Advanced Engineering, ISSN 2250-2459, vol. 5, April 2015.
- [8] D. Jayahar, R. Ranihemamalini, K. Rathnakannan, "Design and Implementation of Single Phase AC-DC Buck-Boost Converter for Power Factor Correction and Harmonic Elimination," International Journal of Power Electronics and Drive System (IJPEDS), vol. 7, no. 3, September 2016, pp. 993~1000, ISSN: 2088-8694, doi: 10.11591/ijpeds.v7i3.7825.
- [9] S. Mageshwari, S. Kanagalakshmi, and H. Rao, "Design and Implementation of Buck-Boost Converter for Residential PV Application," International Journal on Electrical Engineering and Informatics, vol. 9, no. 4, pp. 834-849, December 2017.
- [10] Jos'e M. Blanes, R. Guti'errez, A. Garrig'os, J. L. Liz'an, and J. M. Cuadrado, "Electric VehicleBattery Life Extension using Ultra Capacitors and an FPGA Controlled Interleaved Buck-Boost Converter," IEEE Transaction on Power Electronics, vol. 28, no. 12, December 2013.
- [11] N. A. Ahmed, "Modeling and simulation of ac-dc buck-boost converter fed dc motor with uniform PWM technique," Electric Power Systems Research, ISSN 0378-7796, vol. 73, Issue 3, 2005, pp. 363-372, https://doi.org/10.1016/j.epsr.2004.08.010.
- [12] S. M. Faruk, M. M. Hossain, A. A. Mansur and M. H. Bhuyan, "Improvement of Input Side Current of a Three-Phase Cúk Rectifier," Southeast University Journal of Science and

Engineering (SEUJSE), 1999-1630, vol. 9, no. 1-2, December 2015, pp. 45-51.

- [13] S. M. Faruk, M. M. Hossain, A. A. Mansur and M. H. Bhuyan, "Investigation of Total Harmonic Distortion of a Three-Phase Cúk Rectifier," *Proceedings of the IEEE International Conference on Informatics, Electronics and Vision* (ICIEV 2016), University of Dhaka (DU), Dhaka, Bangladesh, 13-14 May 2016, pp. 385-390.
- [14] J. N. Lou, X. B. Wu, M. L. Zhao, and X.-L.Yan, "A greenswitch controller IC for cascade buck-boost converter with seamless transition over entire input and load range," Microelectronics Journal, vol. 42, no. 10, October 2011, pp. 1151-1163.
- [15] H. M. Mahery and E. Babaei, "Mathematical modeling of buckboost dc-dc converter and investigation of converter elements on transient and steady state responses," International Journal of Electrical Power and Energy Systems. vol. 44, no. 1, January 2013, pp. 949-963.
- [16] S. Narula, B. Singh and G. Bhuvaneswari, "PFC bridgeless converter for welding power supply with improved power quality," 2014 IEEE International Conference on Power Electronics, Drives and Energy Systems (PEDES), 2014, pp. 1-6, doi: 10.1109/PEDES.2014.7041980.
- [17] I. Ahmed, G. Sarowar, and F. S. Azad, "A New Topology Single-Phase Single Switch Non-isolated Buck-Boost Converter with Improved Performance," Przeglad Elektrotechniczny, Vol. 96, 2020, pp. 54-58.
- [18] M. M. S. Khan, M. S. Arifin, M. R. T. Hossain, M. A. Kabir, A. H. Abedin and M. A. Choudhury, "Input switched single phase buck and buck-boost AC-DC converter with improved power quality," 2012 7th International Conference on Electrical and Computer Engineering, BUET, Dhaka, 2012, pp. 189-192.
- [19] G. Sarowar and M. A. Hoque, "High Efficiency Single Phase Switched Capacitor AC to DC Step Down Converter," Procedia-Social and Behavioral Sciences, vol. 195, 2015, pp. 2527-2536.



Istiak Ahmed was born in Bhola, Bangladesh in 1992. He received the BSc and MSc degrees in Electrical and Electronic Engineering (EEE) from Islamic University of Technology (IUT), Board Bazar, Gazipur, Bangladesh ad 2020 respectively

in the years 2015 and 2020 respectively.

From September 2016 to September 2018, he worked as a Lecturer in the Department of Electrical and Electronic Engineering, Sonargaon University (SU), Dhaka, Bangladesh. Currently, he is serving as a Lecturer in the Department of Electrical and Electronic Engineering, Southeast University (SEU), Dhaka, Bangladesh from October 2018.

He is the author of four international journal papers and presented in two IEEE sponsored conferences. His research interests include power electronics, power, and energy.



Muhibul Haque Bhuyan (MIEEE2005–) became a Member (M) of the World Academy of Science, Engineering and Technology in 2005, born in Dhaka, Bangladesh on 25 July 1972. He did his BSc, MSc, and PhD degrees in Electrical

and Electronic Engineering (EEE) from Bangladesh University of Engineering and Technology (BUET), Dhaka, Bangladesh in 1998, 2002, and 2011 respectively.

Currently, he is working as a Professor of the Department of Electrical and Electronic Engineering of Southeast University, Dhaka, Bangladesh. He led this department as the Departmental Chairman from 1st March 2016 to 10 March 2021. Previously, he worked at the Green University of Bangladesh, Dhaka as a Professor and Chairman of the EEE Department; Daffodil International University, Dhaka, Bangladesh as an Assistant Professor and Head of ETE Department; Presidency University, Dhaka, Bangladesh as an Assistant Professor and American International University Bangladesh (AIUB), Dhaka as a Faculty Member since June 1999. He also worked as a Researcher in the Center of Excellence Program of Hiroshima University, Japan from July 2003 to March

2004. He has served as an Adjunct Faculty at AUST, IIUC, EWU, DIU, PU, etc. So far, he has published over 60 research papers in national and international journals and presented over 50 research works at national and international conferences. His research interests include MOS device modeling, biomedical engineering, control system design, online practices of teaching and learning, outcome-based engineering education, assessment, and evaluation. He is a program evaluator of the Board of Accreditation of Engineering and Technical Education (BAETE), Dhaka, Bangladesh under IEB.

Prof. Bhuyan is a Member of IEEE, USA, Executive and Life Member of the Bangladesh Electronics and Informatics Society (BEIS), and Life Fellow of the Institution of Engineers Bangladesh (IEB). He is a regular reviewer and technical/editorial/organizing committee member of several national and international journals and conferences. He was the Organizing Chair of the IEEE 22nd International Conference on Computer and Information Technology (ICCIT) held at Southeast University, Dhaka, Bangladesh during 18-20 December 2019. He is the recipient of the Bangladesh Education Leadership Awards (Best Professor in Electrical Engineering) in 2017 from the South Asian Partnership Awards, Mumbai, India.